

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 07-11-03
Application Type:: Regular
Subject Matter:: Utility
Title:: Pass Gate Multiplexer Circuit with
Reduced Susceptibility to Single
Event Upsets
Attorney Docket Number:: X-1097-1D US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: Three
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Trevor
Middle Name:: J.
Family Name:: Bauer
Street:: 3860 Campo Court
City:: Boulder
State or Province:: Colorado
Postal or Zip Code:: 80301

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Custom r Number::	24309	
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DOMESTIC PRIORITY INFORMATION

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Applic.	Divisional	10/172,836	06-13-02

ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124